

<b>Notice of References Cited</b>	Application/Control No. 10/804,713	Applicant(s)/Patent Under Reexamination YANG, CHAO-HSIANG	
	Examiner Johannes P. Mondt	Art Unit 3663	Page 1 of 1

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*	B	US-6,828,653 B1	12-2004	Castagnetti et al.	257/529
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**NON-PATENT DOCUMENTS**

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	U	Wolf et al, "Silicon Processing for the VLSI Era", Volume 1 -Process Technology, Second Edition, Lattice Press, Sunset Beach, California, ISBN 0-961672-6-1; in particular, pages 798-799.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.